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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Jae-Eun LIM, et al.

ART UNIT: 2812

APPLICATION NO.: 10/735,742

EXAMINER: Isaac, Stanetta D

FILING DATE: December 16, 2003

FOR: METHOD FOR MANUFACTURING SHALLOW TRENCH ISOLATION IN
SEMICONDUCTOR DEVICE

**REVOCATION OF POWER OF ATTORNEY AND
NEW POWER OF ATTORNEY**

COMMISSIONER FOR PATENTS
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SIR:

The undersigned representative of **Hynix Semiconductor Inc.**, an owner by virtue of assignment of the above-identified application, hereby revokes any and all previous Powers of Attorney, and appoints the practitioners associated with the **Customer Number 43569** as Assignee's attorneys with full power of substitution and revocation, to prosecute said patent application, receive any Letters Patent and to take any and all other actions with regard to this patent application and any Letters Patent issuing thereon, and request that all correspondence be sent to **Customer No. 43569** or **Mayer, Brown, Rowe & Maw LLP**, Intellectual Property Group, 1909 K Street, N.W., Washington D.C. 20006-1101.

CERTIFICATION UNDER 37 C.F.R. 3.73(b)

I, the undersigned, certify that I am an individual empowered to act on behalf of **Hynix Semiconductor Inc.**, the assignee of the entire right, title and interest of the above-identified application by virtue of an assignment from the inventor(s).

I further declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code and that such

willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date of Signature: 2005/01/28

Signature:



Name:

CHOI, Hyun-Koo

Title of Signor: Senior Manager